## VISVESVARAYA COLLEGE OF ENGINEERING AND TECHNOLOGY

M.P. PATELGUDA (V), IBRAHIMPATNAM (M), RANGAREDDY DIST-501510
AN UGC AUTONOMOUS INSTITUTION

## EXAMINATION BRANCH

## I M.TECH I SEMESTER (VR24) REGULAR EXAMINATIONS, MAR 2025

## TIMETABLE

TIME: FN: 10.00 AM TO 01.00 PM

BRANCH	DATE AND DAY				
	04-03-2025 AN TUESDAY	06-03-2025 FN THURSDAY	08-03-2025 AN SATURDAY	10-03-2025 FN MONDAY	12-03-2025 AN WEDNESDAY
Embedded Systems	Digital System Design with FPGAs	System Design with Embedded Linux	Wireless Sensor Networks	Advanced Computer Architecture	Research Methodology & IPR
Computer Science & Engineering	Mathematical Foundations of Computer Science	Advanced Data Structures	Database Programming with PL/SQL	Software Quality Engineering	Research Methodology & IPR

DATE: 12-02-2025

CONTROLLER OF EXAMINATIONS

Visvesvaraya College of Engineering & Technology, (AUTONGMOUS)

M.P. Peteiguda (V), Ibrahimpatnam (M), Ranga Reddy (Bist), Telangana-501510. PRINCIPAL

Principal
Wisvesveraya College of Engineering & Technology,
(AUTONOMOUS)

M.P. Patolguda (V), Ibrahimpetnaca (M), Ranga Raddy (Dist), Telangana-601510.